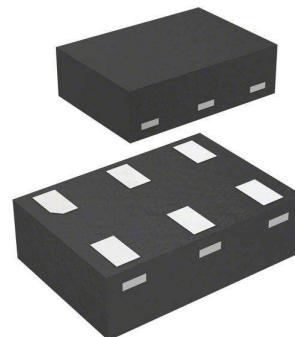


Features

- 60Watts peak pulse power ($t_p = 8/20\mu s$)
- DFN1610 package
- Bidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance ($C_j=0.4\text{pF}$ typ. IO to IO)
- Protection one data/power line to:
- IEC 61000-4-2 $\pm 20\text{kV}$ contact $\pm 20\text{kV}$ air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 3.5A (8/20 μs)



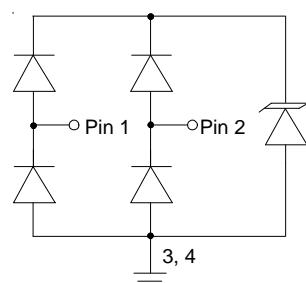
Applications

- Digital Visual Interface (DVI)
- MDDI Ports
- PCI Express
- eSATA Interface

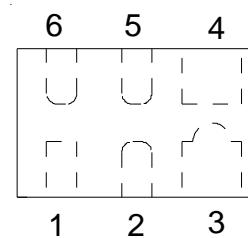
Mechanical Data

- DFN1610 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

Schematic & PIN Configuration



Schematic



DFN1610-6L



Absolute Maximum Rating

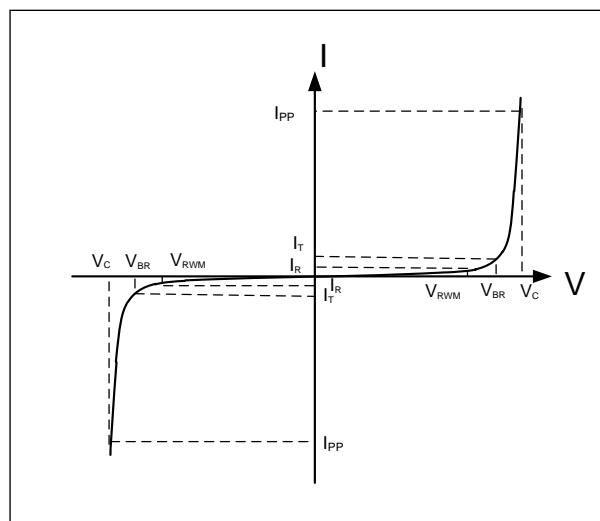
Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P _{PP}	60	Watts
Peak Pulse Current ($t_p = 8/20\mu s$) (note1)	I _{PP}	3.5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	20 20	kV
Lead Soldering Temperature	T _L	260(10seconds)	°C
Junction Temperature	T _J	-55 to + 125	°C
Storage Temperature	T _{stg}	-55 to + 125	°C

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V _{RWM}				5.0	V
Reverse Breakdown Voltage	V _{BR}	I _T =1mA	6.0	8.0		V
Reverse Leakage Current	I _R	V _{RWM} =5V,T=25°C		0.1	0.5	µA
Peak Pulse Current	I _{PP}	t _p =8/20µs			3.5	A
Clamping Voltage	V _C	I _{PP} =3.5A,t _p =8/20µs			15	V
Junction Capacitance	C _j	I _O to I _O V _R = 0V, f = 1MHz		0.4	0.5	pF

Electrical Parameters (TA = 25 °C unless otherwise noted)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current



Note: 8/20µs pulse waveform.



Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

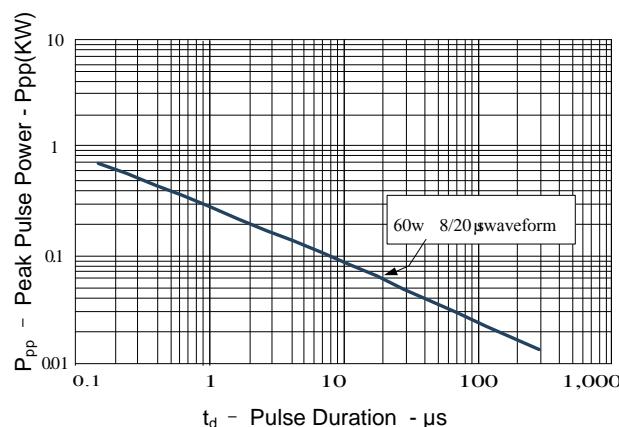


Figure 2: Power Derating Curve

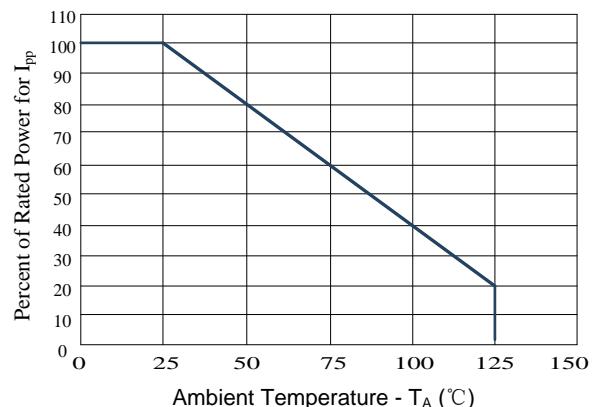


Figure 3: Pulse Waveform

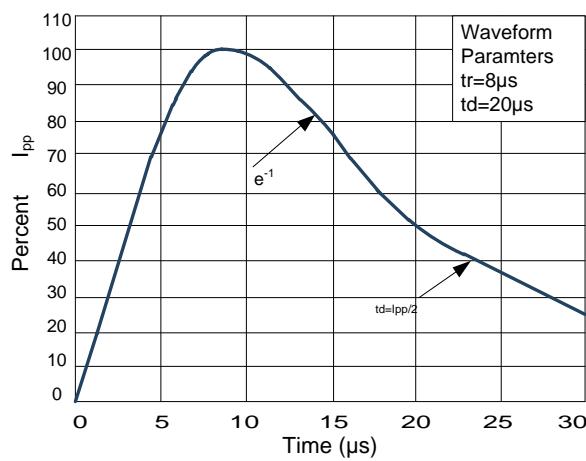


Figure 4: Clamping Voltage vs. I_pp

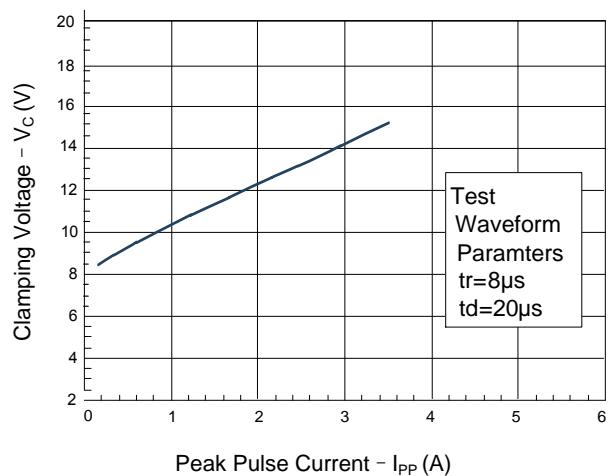


Figure 5: Positive Clamping voltage (TLP)

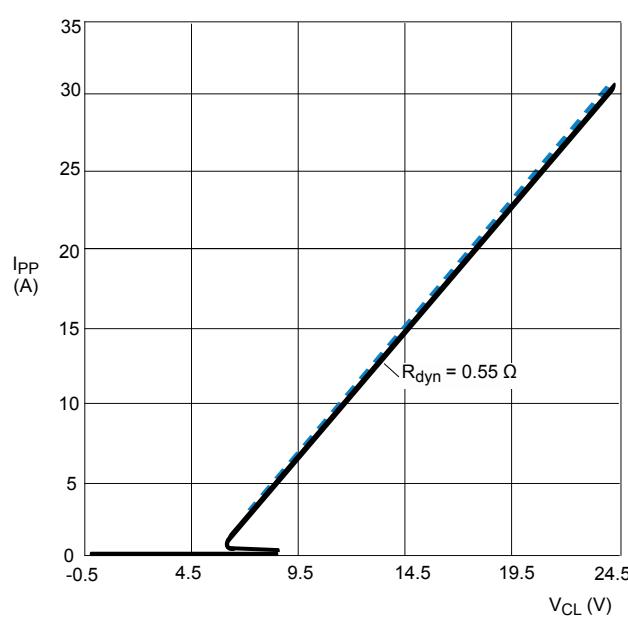
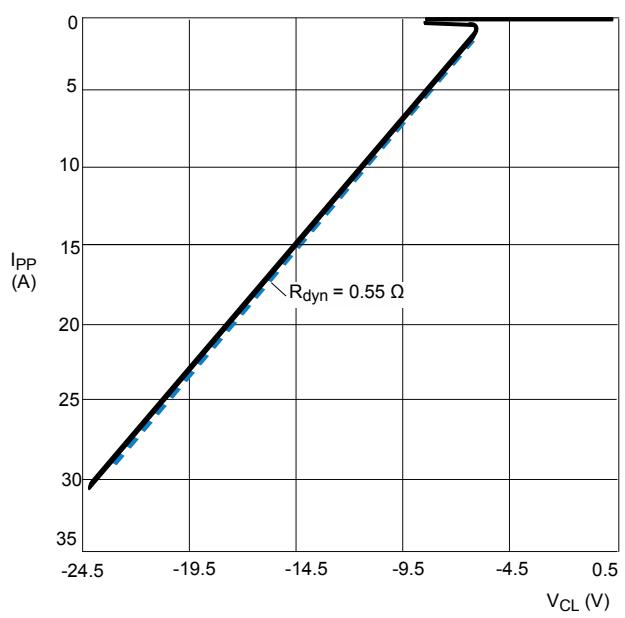
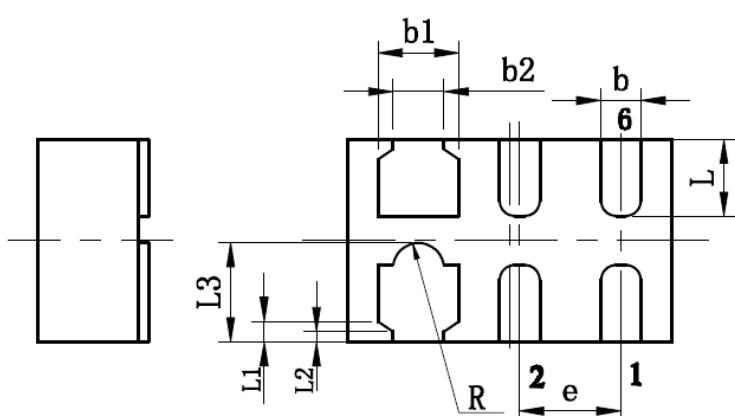
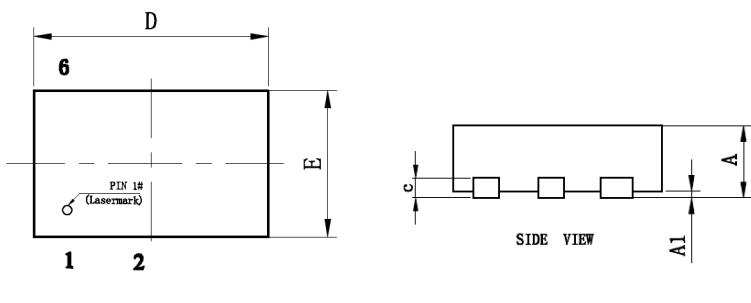


Figure 5: Negative Clamping voltage (TLP)



Outline Drawing – DFN1610



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.50	0.55	0.60
A1	—	0.02	0.05
b	0.15	0.20	0.25
b1	0.35	0.40	0.45
b2	0.20	0.25	0.30
c	0.10	0.15	0.20
D	1.55	1.60	1.65
e	0.50BSC		
E	0.95	1.00	1.05
L	0.33	0.38	0.43
L1	0.100REF		
L2	0.05REF		
L3	0.49REF		
R	0.08	0.13	0.18

Marking

0522P
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Ordering information

Order code	Package	Base qty	Delivery mode
RL16106Q0502UV	DFN1610	3k	Tape and reel

